Tailorpixels

A Professional Manufacturer of Display

Manufacturer Certificated

9001 CERT. No.: 282Q19070712006

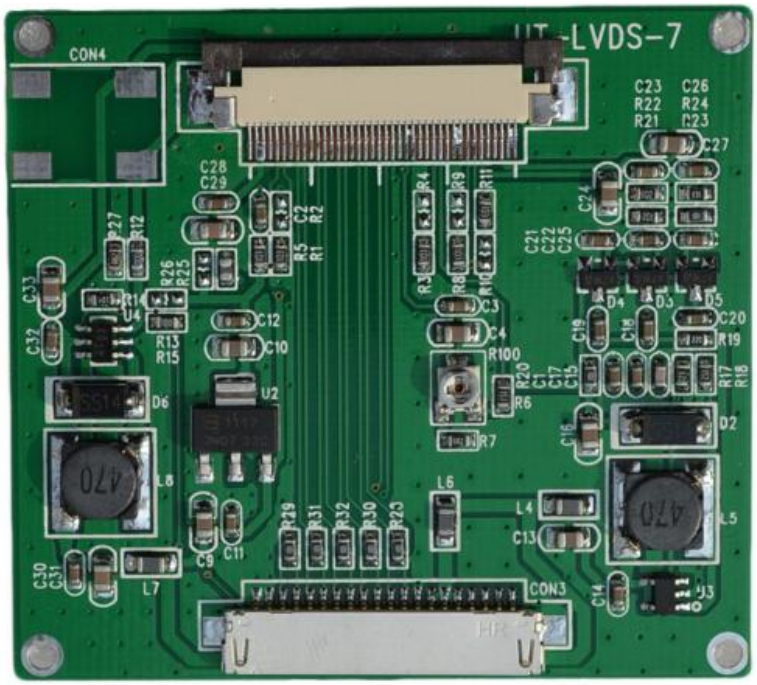
14001 CERT. No.: 282E19070712007

Product Specification

Model: TBJ070LV-01

LVDS Display Board

This module uses ROHS material



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Contents

1. Record of Revision 2
2. General Specifications 3
3. Electrical Characteristics 3
4. PIN-MAP 4
5. Block Diagram 6
6. Mechanical Drawing 6

1.Record of Revision

Version	Revise Date	Content	Remark
V1.0	2012/06/25	-	-

2. General Specifications

Feature		Spec
Characteristics	Size	-
	LCM Resolution supported	800(horizontal)*480(Vertical)
	Interface	LVDS
	Connect type	Connector
	PCBA size	55.0*50.0mm
	LCM connector	40 PIN/0.5S
	LVDS connector	20PIN/DF19-1.0S

3.Electrical Characteristics

Item	Symbol	MIN	Typ	MAX	Unit
Supply Voltage	VDD	-	5.0	-	V
Direct Current	Id	-	250	-	mA
Operating Temperature	TOPR	-20	-	70	℃
Storage Temperature	TSTG	-30	-	80	℃

4.PIN-MAP

4.1 PIN-MAP-LVDS(CON3)

Pin No.	Symbol	Function

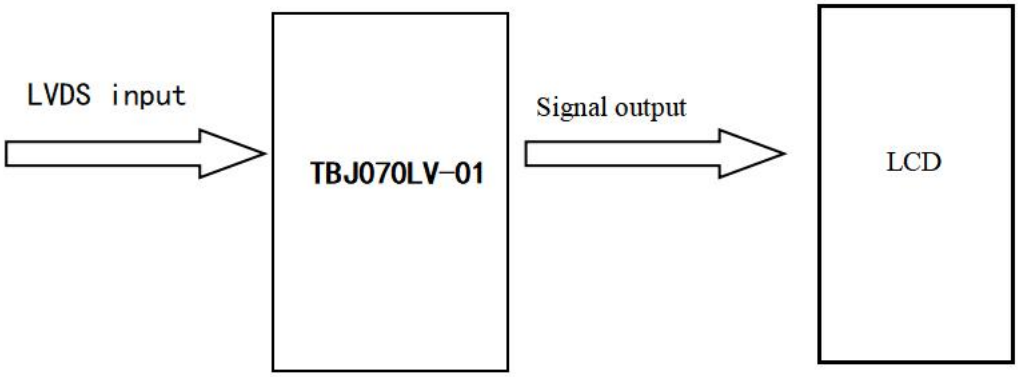
1	ON/OFF	Backlight on/off
2	NC	NC
3	NC	NC
4	NC	NC
5	VCC	Power supply (+5.0V)
6	VCC	Power supply (+5.0V)
7	VCC	Power supply (+5.0V)
8	GND	GND
9	RXCLK+	DCLK+
10	RXCLK-	DCLK—
11	RXIN2+	Channel 2+
12	RXIN2-	Channel 2-
13	RXIN1+	Channel 1+
14	RXIN1-	Channel 1-
15	RXIN0+	Channel 0+
16	RXIN0-	Channel 0—
17	RXIN3+	Channel 3+
18	RXIN3-	Channel 3-
19	GND	GND
20	GND	GND

4.2 PIN-MAP-LCM(CON2)

Pin No.	Symbol	Function
1	VCOM	Common Voltage
2-3	VDD	Power Voltage for digital circuit
4	NC	No connection

5	Reset	Global reset pin
6	STBYB	Standby mode, STBYB = “1”, normal operation STBYB = “0”, timing controller, source driver will turn off,
7	GND	Ground
8	RXIN0-	- LVDS differential data input
9	RXIN0+	+ LVDS differential data input
10	GND	Ground
11	RXIN1-	- LVDS differential data input
12	RXIN1+	+ LVDS differential data input
13	GND	Ground
14	RXIN2-	- LVDS differential data input
15	RXIN2+	+ LVDS differential data input
16	GND	Ground
17	RXCLKIN-	- LVDS differential clock input
18	RXCLKIN+	+ LVDS differential clock input
19	GND	Ground
20	RXIN3-	- LVDS differential data input
21	RXIN3+	+ LVDS differential data input
22	GND	Ground
23-24	NC	No connection
25	GND	Ground
26	NC	No connection
27	DIMO	Backlight CABC controller signal output
28	SELB	6bit/8bit mode select
29	AVDD	Power for Analog Circuit
30	GND	Ground
31-32	LED-	LED Cathode
33	L/R	Horizontal inversion
34	U/D	Vertical inversion
35	VGL	Gate OFF Voltage
36	CABCEN1	CABC H/W enable
37	CABCEN0	CABC H/W enable
38	VGH	Gate ON Voltage
39-40	LED+	LED Anode

5. Block Diagram



6. Mechanical Drawing